

**EXPEDITED PROCEDURE  
AFTER FINAL REJECTION  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Walter Fix et al.  
Serial No.: 10/562,869  
Filed: April 7, 2006  
For: Logic Gate with a Potential-Free Gate Electrode for Organic Integrated Circuits  
Examiner: Eva Y. Montalvo Art Unit: 2814  
Attorney Dkt: 411000-144 Customer No. 27162

**RESPONSE UNDER 37 CFR 1.116**

MS Amendment  
Commissioner for Patents  
Box 1450  
Alexandria, VA 22313-1450

This paper is in response to the Office Action dated May 14, 2008 having a response set to expire on August 14, 2008.

**IN THE CLAIMS**

Amend the claims as shown starting on page 2 of this paper.

Remarks begin on page 4.